

USPAT:USPGPUB:EPQ:PO:DERVENT:BM:IDB

Default operator: AQR

2 and sense ad) (amplifier or transistor)

2 and sense ad) (amplifier or transistor)

Drafts

ISR:

BR:

ISR:

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ISR:

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ISR:

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Pending

Active

L1: (12) (plural ad) amplifier or p

L2: (1172) (plural ad) amplifier or

L3: (12) (2 and sense ad) (ampl

Failed

Saved

Favorites

Tagged (0)

UDC

Queue

Trash

USPAT:USPGPUB:EPQ:PO:DERVENT:BM:IDB

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	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Classif	Inventor	S	D	P	CS	
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20040076045 A1	20040422	14	Semiconductor memory device	365/200			Hira, Masayuki et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030224624 A1	20031204	17	Method of forming a semiconductor device and structure therefor	438/800			Gay, Michael J.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030098674 A1	20030529	13	Wideband regulator with fast transient suppression circuitry	323/276			Ostrom, Kenneth A.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030054776 A1	20030320	12	Amplifier power detection circuitry	455/115.1	455/126		Hoch, James Burr	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020070721 A1	20020613	13	Wideband regulator with fast transient suppression circuitry	323/284			Ostrom, Kenneth A.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6661212 B2	20031209	12	Wideband regulator with fast transient suppression circuitry	323/276	323/274; 327/538; 363/607		Ostrom, Kenneth A.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6396298 B1	20020528	15	Active feedback pulsed measurement method	324/769	324/158.1		Young, Albert M. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6166766 A	20001226	9	Sensing circuit for capturing a pixel signal	348/241	327/90		Moore, Kendall G. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5867053 A	19990202	9	Multiplexed output circuit and method of operation thereof	327/407	327/408; 327/409		Engles, Bruce E. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
10	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4916700 A	19900410	15	Semiconductor storage device	714/718	365/201; 714/719		Ito, Kazuya et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
11	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4811295 A	19890307	15	High speed serial input/output semiconductor memory	365/189.05	365/219; 365/221; 365/231.09		Shinoda, Takashi	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>